

Patent Assignment Abstract of Title

Total Assignments: 1**Application #:** 10014359 **Filing Dt:** 12/14/2001**Patent #:** NONE**Issue Dt:****PCT #:** NONE**Publication #:** US20020080825**Pub Dt:** 06/27/2002**Inventors:** Michael Joachim Wolf, Werner Beisel, Jurgen Hohn**Title:** Method and compensation module for the phase compensation of clock signals**Assignment: 1****Reel/Frame:** 012379 /
0160**Received:**
12/21/2001**Recorded:**
12/14/2001**Mailed:**
02/11/2002**Pages:**
2**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignors:** WOLF, MICHAEL JOACHIM**Exec Dt:** 11/28/2001BEISEL, WERNER**Exec Dt:** 11/28/2001HOEHN, JUERGEN**Exec Dt:** 11/28/2001**Assignee:** ALCATEL

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<u>L7</u>	L6 and delay	9	<u>L7</u>
<u>L6</u>	l1 and compensation and phase and internal near clock	9	<u>L6</u>
<u>L5</u>	L1 and delay near circuit and compensation near phase	4	<u>L5</u>
<u>L4</u>	L1 and first near delay and second near delay and compensation	0	<u>L4</u>
<u>L3</u>	L2 and first near delay and second near delay	0	<u>L3</u>
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<u>L22</u>	L21 and compensation near phase	1	<u>L22</u>
<u>L21</u>	L20 and first near internal and second near internal	175	<u>L21</u>
<u>L20</u>	internal near clock and first adj delay and second adj delay	1101	<u>L20</u>
<u>L19</u>	L18 and first near delay and second near delay	6	<u>L19</u>
<u>L18</u>	L17 and second near clock	6	<u>L18</u>
<u>L17</u>	L16 and first near clock	6	<u>L17</u>
<u>L16</u>	L15 and clock	16	<u>L16</u>

<u>L15</u>	L14 and phase and compensation	16	<u>L15</u>
<u>L14</u>	first near delay and second near delay and first near internal and second near internal	275	<u>L14</u>
<u>L13</u>	first near delay and second near delay and first near internal and second near internal	275	<u>L13</u>
<u>L12</u>	L11	1	<u>L12</u>
<u>L11</u>	L10 and second near delay	1	<u>L11</u>
<u>L10</u>	l9 and first near delay	1	<u>L10</u>
<u>L9</u>	L8 and phase near compensation	1	<u>L9</u>
<u>L8</u>	first near internal near clock and second near internal near clock	474	<u>L8</u>
<u>L7</u>	L6 and phase near adjustment	3	<u>L7</u>
<u>L6</u>	L5 and second near delay	5	<u>L6</u>
<u>L5</u>	L4 and first near delay	5	<u>L5</u>
<u>L4</u>	L3 and internal near clock	9	<u>L4</u>
<u>L3</u>	L1 and compensation	70	<u>L3</u>
<u>L2</u>	L1 and phase adj compensation near module	1	<u>L2</u>
<u>L1</u>	370/517.ccls.	439	<u>L1</u>

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